Notice of References Cited

Application/Control No.

O9/989,482

Examiner

William M. Treat

Applicant(s)/Patent Under
Reexamination
WISHNEUSKY

Art Unit
Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,728,959	04-2004	Merkey, Jeffrey V.	718/102
	В	US-			
	С	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	к	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0			7		
	Р					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Fiske et al., "Thread Prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors", Proceedings of the First IEEE Symposium on High Performance Computer Architecture, IEEE, 01/22/199501/25/1995, pp. 210-221.				
	V					
	w					
	х					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

BEST AVAILABLE COPY